

Specification of Cyber Physical Systems Based on Clock Theory

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Abstract

Cyber Physical Systems are integrations of computing, communication and control systems, and CPS make it possible to link the physical world and discrete world much more tightly and accurately. Though the appearance of CPS has triggered a great revolution to the electronic, information and internet technology, many problems still need to be solved when using the existing theories and technologies especially the description and analysis of time in Cyber Physical Systems. Clock theory puts forward the description of time and corresponding mechanism. This paper aims to apply the clock theory to the specification of Cyber Physical Systems.

Keywords: Cyber Physical Systems, continuous and discrete, clock, time analysis.

1 Introduction

Time[13] is a very important element especially in Cyber Physical Systems. Cyber Physical Systems are dynamic systems that exhibit both continuous and discrete[12] dynamic behavior. And the continuously bilateral interaction between discrete events and continuous time flow makes it hard to know the dynamic and real-time[10, 11] feature of the system, so keeping synchronization is really important work. We would prefer a unified clock to so many different time lines in the same system.

And when it comes to specification of Cyber Physical Systems, K. L. Man, and R. R. H. Schiffelers have done much constructive work that the thesis *Formal Specification and Analysis of Hybrid Systems*[8] by them introduces a hybrid process algebra named χ formalism. In the thesis, they not only elaborate the syntax and semantics of the χ specification, but also the examples and translation between other formalisms and χ . Bottle filling system is one of the cases in the thesis, but in χ , authors tend to make all the parameters and events together. Although every subsystem is clear, the interaction of them is not so good. *Link Continuous World with Discrete World*[9] by Jifeng He has a detailed specification on *time*, description of dynamic operations of continuous world, and every event is combined with a clock. This paper aims to use clock to specify the Cyber Physical Systems by cases—railroad crossing control system and bottle filling system[8].

2 Clock Theory

Clock theory[9] puts forward the possibility to describe the event in physical world by a much clearer *time* description, and can analyze, record the event by clock as well. To specify Cyber Physical Systems based on clock, the time description is clearer to every event and can link continuous world with discrete world more tightly. The definition and linking mechanism of clock theory is provided as below.

DEFINITION 1 A clock c is an increasing sequence of real numbers, $c[1]$ stands for the first element of c . We define its low and high rates by:

$$\Delta(c) =_{df} \mathbf{inf}\{(c[i+1] - c[i]) | i \in Nat\} \quad (1)$$

$$\nabla(c) =_{df} \mathbf{sup}\{(c[i+1] - c[i]) | i \in Nat\} \quad (2)$$

DEFINITION 2 If c is a healthy clock, it does not speed up infinitely. Then

$$\Delta(c) > 0$$

DEFINITION 3 (Partial Order in Clock) If c runs faster than d . For all $i \in Nat$, $c[i] \leq d[i]$. Then relation of c and d can be denoted by:

$$c \preceq d \quad (3)$$

Lemma 1

$$c \preceq c'$$

DEFINITION 4 Let c and d be clocks. We define the transition latency between the two clocks as

$$\rho(c, d) =_{df} \mathbf{sup}\{|c[i] - d[i]| | i \in Nat\} \quad (4)$$

Lemma 2

$$\rho(c, d) \geq 0$$

DEFINITION 5 (Local Clock and Global Clock) Let l be a label denoting a location, and c a clock. Then $l:c$ denotes clock c that locates at l .

$$l : c \preceq l : d =_{df} (c \preceq d) \quad (5)$$

$$\rho(l : c, l : d) =_{df} \rho(c, d) \quad (6)$$

A global clock $[l:c]$ is defined as an equivalent class of local clocks,

$$\rho([l_1 : c_1], [l_2 : c_2]) =_{df} \rho(l_1 : c_1, l_2 : c_2) \quad (7)$$

DEFINITION 6 Some dynamic features of continuous variable can be described better as follows.

climb(u, r) is introduced to describe the time instants when the value of u rises up to r .

drop(u, r) is introduced to describe the time instants when the value of u falls below r .

DEFINITION 7 (Linking Mechanism) Here c is a clock with $c[1] > 0$, and x_0 is an initial value. In this equation, we assign the value of continuous variable u to discrete variable x at the every instant of clock c .

$$x = u \text{ every } c \text{ init } x_0 \quad (8)$$

In differential equation, u is a continuous variable, u_0 is an initial value, and f is an expression. $(\dot{u} = f) \wedge (u(0) = u_0)$ is the relation between u and f , then

$$\dot{u} = f \text{ init } u_0 \quad (9)$$

Let e be an event. $\mathbf{clock}(e)$ denotes the clock that records the time instants when e occurs. And $\mathbf{clock}(\mathbf{event}(c))$ denotes the time instants that the event takes place at every time instant $c[i]$.

3 Case Study 1: Railroad crossing control system

As Figure 1 shows below, four train stations named Station 1, Station 2, Station 3, Station 4 are connected by two orthogonal railways. Train a goes through between Station 1 and Station 2, while train b goes through between Station 3 and Station 4. Four sensors named A, B, C, D are installed several meters away from the crossing area to detect whether a train approaches or not. A radio transmission center(Figure 2) can permit the train to go through to guarantee a safe traffic. Suppose that train a and train b set out from Station 1 and Station 4 separately when $t = 0$. When $t = t_a$, train a reaches sensor A, and train a is permitted to go through since the railway is safe. And when $t = t_b$, b arrives and asks for entering, the request is declined. All the permission, refusal and waiting time should be finished in $[t_1, t_2]$. v_a, v_b represents the speed of train a and train b.

We should promise that the system will never deadlock; two trains will never collide together or enter the crossing area at the same time only when it is permitted.

We use continuous variable \mathbf{ua} and \mathbf{ub} to describe the running time for a and waiting time for b. And control unit use event \mathbf{pass} and event \mathbf{wait} to control the condition of trains.

To train a:

$$\begin{aligned} \mathit{climb}(\mathbf{ua}, t_2 - \varepsilon) &\preceq \mathit{clock}(\mathit{pass}) \\ \rho(\mathit{climb}(\mathbf{ua}, t_2 - \varepsilon), \mathit{clock}(\mathit{pass})) &\leq \varepsilon \\ \mathit{drop}(\mathbf{ua}, t_1 + \varepsilon) &\preceq \mathit{clock}(\mathit{wait}) \\ \rho(\mathit{climb}(\mathbf{ua}, t_2 - \varepsilon), \mathit{clock}(\mathit{wait})) &\leq \varepsilon \\ \varepsilon &\leq \min(t_2 - t_a, t_a - t_1)/2 \end{aligned}$$

To train b:

$$\begin{aligned} \mathit{climb}(\mathbf{ub}, t_2 - \zeta) &\preceq \mathit{clock}(\mathit{pass}) \\ \rho(\mathit{climb}(\mathbf{ub}, t_2 - \zeta), \mathit{clock}(\mathit{pass})) &\leq \zeta \\ \mathit{drop}(\mathbf{ub}, t_1 + \zeta) &\preceq \mathit{clock}(\mathit{wait}) \end{aligned}$$

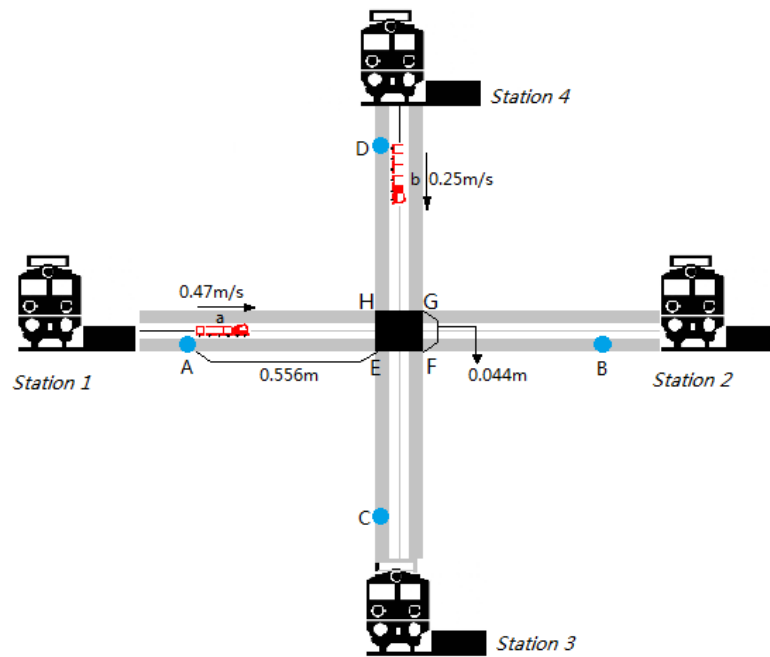


Figure 1. Railroad crossing control system

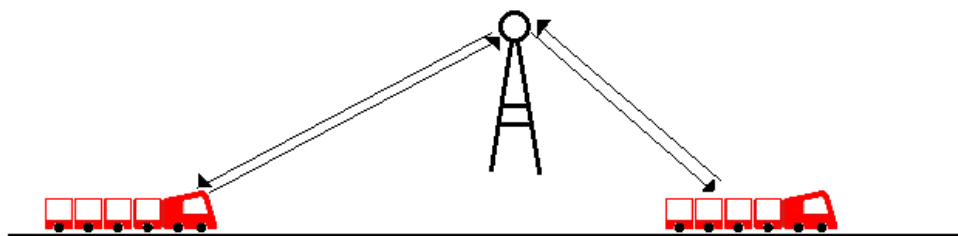


Figure 2. Radio transmission center

$$\rho(\text{climb}(ub, t_2 - \zeta), \text{clock}(\text{wait})) \leq \zeta$$

$$\zeta \leq \min(t_2 - t_b, t_b - t_1)/2$$

And the two events have noninterference.

$$\text{clock}(\text{pass})[1] > 0$$

$$\text{clock}(\text{pass}) \preceq \text{clock}(\text{wait}) \preceq \text{clock}(\text{pass})'$$

$$\text{clock}(\text{pass}) \wedge \text{clock}(\text{wait}) = \emptyset$$

Consider the condition of sensors, the sensors detect event **arrive** and **leave**. Here **sa** represents the response time of sensor A and B:

$$\text{climb}(\text{sa}, t_2 - \varepsilon) \preceq \text{clock}(\text{arrive})$$

$$\rho(\text{climb}(\text{sa}, t_2 - \varepsilon), \text{clock}(\text{arrive})) \leq \varepsilon/2$$

$$\text{drop}(\text{sa}, t_1 + \varepsilon) \preceq \text{clock}(\text{leave})$$

$$\rho(\text{climb}(\text{sa}, t_2 - \varepsilon), \text{clock}(\text{leave})) \leq \varepsilon/2$$

Here **sb** represents the response time of sensor C and D:

$$\text{climb}(\text{sb}, t_2 - \varepsilon) \preceq \text{clock}(\text{arrive})$$

$$\rho(\text{climb}(\text{sb}, t_2 - \varepsilon), \text{clock}(\text{arrive})) \leq \zeta/2$$

$$\text{drop}(\text{sb}, t_1 + \varepsilon) \preceq \text{clock}(\text{leave})$$

$$\rho(\text{climb}(\text{sb}, t_2 - \varepsilon), \text{clock}(\text{leave})) \leq \zeta/2$$

Since $\text{clock}(a), \text{clock}(b), \text{clock}(sa), \text{clock}(sb), \text{clock}(sc)$, and $\text{clock}(sd)$ have deviation from each other. We define that latency between train a and sensor A,B is δ ms, and the latency between train b and sensor C,D is σ ms.

$$|\text{clock}(a) - \text{clock}(sa)| < \delta$$

$$|\text{clock}(a) - \text{clock}(sb)| < \delta$$

$$|\text{clock}(sa) - \text{clock}(sb)| < \delta$$

$$|\text{clock}(b) - \text{clock}(sc)| < \sigma$$

$$|\text{clock}(b) - \text{clock}(sd)| < \sigma$$

$$|\text{clock}(sc) - \text{clock}(sd)| < \sigma$$

For train a, when its request is permitted:

$$\text{clock}(\text{arrive}) \preceq \text{clock}(\text{pass})$$

$$\rho(\text{clock}(\text{arrive}), \text{clock}(\text{pass})) \leq \varepsilon/2$$

when its request is declined:

$$\begin{aligned} \text{clock}(\text{arrive}) &\preceq \text{clock}(\text{wait}) \\ \rho(\text{clock}(\text{arrive}), \text{clock}(\text{wait})) &\leq \varepsilon/2 \end{aligned}$$

For train b, when its request is permitted:

$$\begin{aligned} \text{clock}(\text{arrive}) &\preceq \text{clock}(\text{pass}) \\ \rho(\text{clock}(\text{arrive}), \text{clock}(\text{pass})) &\leq \zeta/2 \end{aligned}$$

when its request is declined:

$$\begin{aligned} \text{clock}(\text{arrive}) &\preceq \text{clock}(\text{wait}) \\ \rho(\text{clock}(\text{arrive}), \text{clock}(\text{wait})) &\leq \zeta/2 \end{aligned}$$

When train has gone through the crossing, control unit needs to inform the other train to go through.

If train a leaves, then:

$$\begin{aligned} \text{clock}(\text{leave}) &\preceq \text{clock}(\text{pass}) \\ \rho(\text{clock}(\text{leave}), \text{clock}(\text{pass})) &\leq \varepsilon/2 \end{aligned}$$

If train b leaves, then:

$$\begin{aligned} \text{clock}(\text{leave}) &\preceq \text{clock}(\text{pass}) \\ \rho(\text{clock}(\text{leave}), \text{clock}(\text{pass})) &\leq \zeta/2 \end{aligned}$$

4 Case Study 2: Bottle filling system

As Figure 3 shows below, the bottle filling system[8] consists of a liquid storage tank and two identical filling lines. The two incoming flows are liquid and acid, and the two outgoing flows are to fill the bottles on the assembly-line. A control system keeps the volume of the liquid storage tank between 2 and 10, and pH level of the liquid in the storage tank between 7 and 7.1. When liquid in tank becomes less acidic, the incoming flow of acid will be dribbled down to correct the situation. The volume of each bottle is 2, when a bottle is filled, the filling lines should stop and wait a period t_{tr} to transport a new bottle, and when the volume of the storage tank is under 0.7, it can not afford to support the filling lines and force them to stop. Since the change of molar quantity, molar concentration and storage tank volume is continuous, the incoming and outgoing flows are discrete, and we can specify the system by clock.

Some necessary parameters for the system are in Table 1.

First, we consider the volume control system; \mathbf{u} is the continuous variable which denotes the volume of incoming liquid; **inflow_off** and **inflow_on** are events which control the incoming flow of liquid.

$$\text{climb}(u, V_{TH} - \epsilon) \preceq \text{clock}(\text{inflow_off})$$

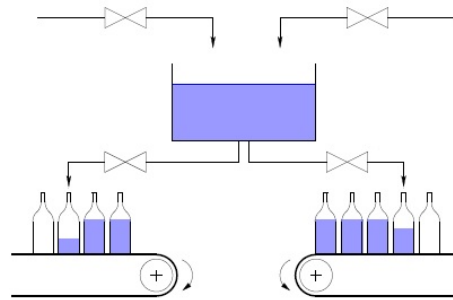


Figure 3. Bottle filling system

Table 1. Parameter list for Bottle filling system

Parameter	Value
a	speed of volume change in storage tank
b	speed of pH change in storage tank
d	speed of volume change in storage tank
I_1	current volume in storage tank
I_2	current pH level in storage tank
V_{TH}	10
V_{TL}	2
pH_H	7.1
pH_L	7
V_H	0.7
V_L	0-0.5

$$\rho(\text{climb}(u, V_{TH} - \epsilon), \text{clock}(\text{inflow_off})) \leq \epsilon/a$$

$$\text{drop}(u, V_{TL} + \epsilon) \preceq \text{clock}(\text{inflow_on})$$

$$\rho(\text{drop}(u, V_{TL} + \epsilon), \text{clock}(\text{inflow_on})) \leq \epsilon/a$$

$$\epsilon \leq \min(V_{TH} - I_1, I_1 - V_{TL})/2$$

And the two events have noninterference.

$$\text{clock}(\text{inflow_off})[1] > 0$$

$$\text{clock}(\text{inflow_off}) \preceq \text{clock}(\text{inflow_on}) \preceq \text{clock}(\text{inflow_off})'$$

$$\text{clock}(\text{inflow_off}) \wedge \text{clock}(\text{inflow_on}) = \emptyset$$

Then, we consider the pH control system; w is the continuous variable which denotes the pH change of storage tank; **acid_off** and **acid_on** are events which control the incoming flow of acid.

$$\text{climb}(w, pH_H - \xi) \preceq \text{clock}(\text{acid_on})$$

$$\rho(\text{climb}(w, pH_H - \xi), \text{clock}(\text{acid_on})) \leq \xi/b$$

$$\text{drop}(w, pH_L + \xi) \preceq \text{clock}(\text{acid_off})$$

$$\begin{aligned} \rho(\text{drop}(w, pH_L + \xi), \text{clock}(\text{acid_off})) &\leq \xi/b \\ \xi &\leq \min(pH_H - I_2, I_2 - pH_L)/2 \end{aligned}$$

And the two events have noninterference.

$$\begin{aligned} \text{clock}(\text{acid_off})[1] &> 0 \\ \text{clock}(\text{acid_off}) &\preceq \text{clock}(\text{acid_on}) \preceq \text{clock}(\text{acid_off})' \\ \text{clock}(\text{acid_off}) \wedge \text{clock}(\text{acid_on}) &= \emptyset \end{aligned}$$

Next, we should consider the filling control system. v_T is the continuous variable which denotes the volume change of storage tank, and $high_{TANK}$, low_{TANK} is one of the decisive events which control the filling lines. $high_{TANK}$ means that storage tank can fill bottles now, while low_{TANK} means that storage tank has little water and can not fill bottles contemporarily.

$$\begin{aligned} \text{climb}(v_T, V_H - \lambda) &\preceq \text{clock}(\text{high}_{TANK}) \\ \rho(\text{climb}(v_T, V_H - \lambda), \text{clock}(\text{high}_{TANK})) &\leq \lambda/d \\ \text{drop}(v_T, V_L + \lambda) &\preceq \text{clock}(\text{low}_{TANK}) \\ \rho(\text{drop}(v_T, V_L + \lambda), \text{clock}(\text{low}_{TANK})) &\leq \lambda/d \\ \lambda &\leq \min(V_H - I_1, I_1 - V_L)/2 \end{aligned}$$

While the other important element influencing the filling lines is the bottle, when a bottle is filled, t_{tr_start} event starts and after the period, a new bottle comes and fills it until the $high_{BOTTLE}$ event starts or there is a low_{TANK} .

$$\begin{aligned} \text{clock}(t_{tr_start}) &\preceq \text{clock}(t_{tr_over}) \preceq (\text{clock}(\text{new}_{BOTTLE}) \wedge \text{clock}(\text{high}_{TANK})) \\ &\preceq \text{clock}(\text{filling_on}) \preceq (\text{clock}(\text{high}_{BOTTLE}) \vee \text{clock}(\text{low}_{TANK})) \\ &\preceq \text{clock}(\text{filling_off}) \preceq \text{clock}(t_{tr_start})' \end{aligned}$$

And the two events have noninterference.

$$\begin{aligned} \text{clock}(\text{filling_off})[1] &> 0 \\ \text{clock}(\text{filling_off}) &\preceq \text{clock}(\text{filling_on}) \preceq \text{clock}(\text{filling_off})' \\ \text{clock}(\text{filling_off}) \wedge \text{clock}(\text{filling_on}) &= \emptyset \end{aligned}$$

In the equations, \mathbf{u} , \mathbf{w} , v_T are continuous variables, and \mathbf{a} , \mathbf{b} , \mathbf{d} are discrete variables.

$$\begin{aligned} \dot{u} &= a \text{ init } a_0 \\ \dot{w} &= b \text{ init } b_0 \\ \dot{v}_T &= d \text{ init } d_0 \\ a &= u \text{ every } c \text{ init } a_0 \\ b &= w \text{ every } c \text{ init } b_0 \\ d &= v_T \text{ every } c \text{ init } d_0 \end{aligned}$$

5 Conclusion

Speaking of specification of Cyber Physical Systems, dealing with the continuous and discrete behavior dynamically is the tough work we have been trying to solve better. And the synchronization of all the subsystems in the whole system is also a difficulty.

Using clock to specify Cyber Physical Systems can give a more detailed description of every subsystem and give a much more considerate observation of the time line and sequence of every event.

It is brilliant to connect the event with clock, while it is difficult to link so many local clocks with the global clock. In my point of view, it is always hard work to make local clocks keeping consistent with the global clock, and the verification of the security and accuracy of the synchronization is very complicated, and we need more ideas on this tough work.

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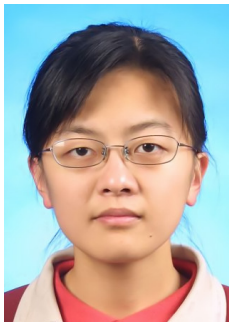
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